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**VLSI Design** - K. Lal Kishore 2013-12-30

Aimed primarily for undergraduate students pursuing courses in VLSI design, the book emphasizes the physical understanding of underlying principles of the subject. It not only focuses on circuit design process obeying VLSI rules but also on technological aspects of Fabrication. VHDL modeling is discussed as the design engineer is expected to have good knowledge of it. Various Modeling issues of VLSI devices are focused which includes necessary device physics to the required level. With such an in-depth coverage and practical approach practising engineers can also use this as ready reference. Key features: Numerous practical examples. Questions with solutions that reflect the common doubts a beginner encounters. Device Fabrication Technology. Testing of CMOS device BiCMOS Technological issues. Industry trends. Emphasis on VHDL.

**PULSE AND DIGITAL CIRCUITS** - A. ANAND KUMAR 2008-02-12

The second edition of this well-received text continues to provide a coherent and comprehensive coverage of Pulse and Digital Circuits, suitable as a textbook for use by undergraduate students pursuing courses in Electrical and Electronics Engineering, Electronics and Communication Engineering, Electronics and Instrumentation Engineering, and Telecommunication Engineering. It presents clear explanations of the operation and analysis of semiconductor pulse circuits. Practical pulse circuit design methods are investigated in detail. The book provides numerous fully worked-out, laboratory-tested examples to give students a solid grounding in the related design concepts. It includes a number of classroom-tested problems to encourage students to apply theory in a logical fashion. Review questions, fill in the blanks, and multiple choice questions offer the students the opportunity to test their understanding of the text material. This text will be also appropriate for self-study by AMIE and IETE students. NEW TO THIS EDITION : • Includes two new chapters—Logic Gates and Logic Families—to meet the curriculum requirements. • Provides short questions with answers at the end of each chapter. • Presents several new illustrations, examples and exercises

**Analog Integrated Circuit Design** - Tony Chan Carusone 2012

The 2nd Edition of Analog Integrated Circuit Design focuses on more coverage about several types of circuits that have increased in importance in the past decade. Furthermore, the text is enhanced with material on CMOS IC device modeling, updated processing layout and expanded coverage to reflect technical innovations. CMOS devices and circuits have more influence in this edition as well as a reduced amount of text on BiCMOS and bipolar information. New chapters include topics on frequency response of analog ICs and basic theory of feedback amplifiers.

[The Design and Analysis of VLSI Circuits](#) - Lance A. Glasser 1985

**Integrated Circuit Design** - Neil H. E. Weste 2011

This edition presents broad and in-depth coverage of the entire field of modern CMOS VLSI Design. The authors draw upon extensive industry and classroom experience to introduce today's most advanced and effective chip design practices.

**Modern VLSI Design** - Wayne Wolf 2002-01-14

For Electrical Engineering and Computer Engineering courses that cover the design and technology of very

large scale integrated (VLSI) circuits and systems. May also be used as a VLSI reference for professional VLSI design engineers, VLSI design managers, and VLSI CAD engineers. Modern VLSI Design provides a comprehensive “bottom-up” guide to the design of VLSI systems, from the physical design of circuits through system architecture with focus on the latest solution for system-on-chip (SOC) design. Because VLSI system designers face a variety of challenges that include high performance, interconnect delays, low power, low cost, and fast design turnaround time, successful designers must understand the entire design process. The Third Edition also provides a much more thorough discussion of hardware description languages, with introduction to both Verilog and VHDL. For that reason, this book presents the entire VLSI design process in a single volume.

**VLSI Design** - Debaprasad Das 2016-01-15

The second edition of VLSI Design is a comprehensive textbook designed for undergraduate students of electrical, electronics, and electronics and communication engineering. It provides a thorough understanding of the fundamental concepts and design of VLSI systems.

**Digital Vlsi Design** - Singh Ajay Kumar 2010-11-01

*The Sparse Fourier Transform* - Maitham Hassanieh 2018-02-27

The Fourier transform is one of the most fundamental tools for computing the frequency representation of signals. It plays a central role in signal processing, communications, audio and video compression, medical imaging, genomics, astronomy, as well as many other areas. Because of its widespread use, fast algorithms for computing the Fourier transform can benefit a large number of applications. The fastest algorithm for computing the Fourier transform is the Fast Fourier Transform (FFT), which runs in near-linear time making it an indispensable tool for many applications. However, today, the runtime of the FFT algorithm is no longer fast enough especially for big data problems where each dataset can be few terabytes. Hence, faster algorithms that run in sublinear time, i.e., do not even sample all the data points, have become necessary. This book addresses the above problem by developing the Sparse Fourier Transform algorithms and building practical systems that use these algorithms to solve key problems in six different applications: wireless networks; mobile systems; computer graphics; medical imaging; biochemistry; and digital circuits. This is a revised version of the thesis that won the 2016 ACM Doctoral Dissertation Award.

*Modern Semiconductor Devices for Integrated Circuits* - Chenming Hu 2010

Modern Semiconductor Devices for Integrated Circuits, First Edition introduces readers to the world of modern semiconductor devices with an emphasis on integrated circuit applications. KEY TOPICS: Electrons and Holes in Semiconductors; Motion and Recombination of Electrons and Holes; Device Fabrication Technology; PN and Metal-Semiconductor Junctions; MOS Capacitor; MOS Transistor; MOSFETs in ICs—Scaling, Leakage, and Other Topics; Bipolar Transistor. MARKET: Written by an experienced teacher, researcher, and expert in industry practices, this succinct and forward-looking text is appropriate for anyone interested in semiconductor devices for integrated circuits, and serves as a suitable reference text for practicing engineers.

[Device Electronics for Integrated Circuits](#) - Richard S. Muller 1986

This Second Edition provides all the required information for a course in modern device electronics taken

by undergraduate electrical engineers. Offers major new coverage of silicon technology, adds several topics in basic semiconductor physics not treated previously, and introduces Hall-effect sensors. The chapters on MOSFET have been entirely updated, focusing on mobility variations and threshold-voltage dependence. Additional topics include VLSI devices, short channel effects, and computer modeling.

**Electromigration Inside Logic Cells** - Gracieli Posser 2016-11-26

This book describes new and effective methodologies for modeling, analyzing and mitigating cell-internal signal electromigration in nanoCMOS, with significant circuit lifetime improvements and no impact on performance, area and power. The authors are the first to analyze and propose a solution for the electromigration effects inside logic cells of a circuit. They show in this book that an interconnect inside a cell can fail reducing considerably the circuit lifetime and they demonstrate a methodology to optimize the lifetime of circuits, by placing the output, V<sub>dd</sub> and V<sub>ss</sub> pin of the cells in the less critical regions, where the electromigration effects are reduced. Readers will be enabled to apply this methodology only for the critical cells in the circuit, avoiding impact in the circuit delay, area and performance, thus increasing the lifetime of the circuit without loss in other characteristics.

**Analysis and Design of Digital Integrated Circuits** - David A. Hodges 2003

The third edition of Hodges and Jackson's Analysis and Design of Digital Integrated Circuits has been thoroughly revised and updated by a new co-author, Resve Saleh of the University of British Columbia. The new edition combines the approachability and concise nature of the Hodges and Jackson classic with a complete overhaul to bring the book into the 21st century. The new edition has replaced the emphasis on BiPolar with an emphasis on CMOS. The outdated MOS transistor model used throughout the book will be replaced with the now standard deep submicron model. The material on memory has been expanded and updated. As well the book now includes more on SPICE simulation and new problems that reflect recent technologies. The emphasis of the book is on design, but it does not neglect analysis and has as a goal to provide enough information so that a student can carry out analysis as well as be able to design a circuit. This book provides an excellent and balanced introduction to digital circuit design for both students and professionals.

**Digital Integrated Circuits** - Jan M. Rabaey 1996

Beginning with discussions on the operation of electronic devices and analysis of the nucleus of digital design, the text addresses: the impact of interconnect, design for low power, issues in timing and clocking, design methodologies, and the effect of design automation on the digital design perspective.

**CMOS** - R. Jacob Baker 2008

This edition provides an important contemporary view of a wide range of analog/digital circuit blocks, the BSIM model, data converter architectures, and more. The authors develop design techniques for both long- and short-channel CMOS technologies and then compare the two.

**Design of 3D Integrated Circuits and Systems** - Rohit Sharma 2018-09-03

Three-dimensional (3D) integration of microsystems and subsystems has become essential to the future of semiconductor technology development. 3D integration requires a greater understanding of several interconnected systems stacked over each other. While this vertical growth profoundly increases the system functionality, it also exponentially increases the design complexity. Design of 3D Integrated Circuits and Systems tackles all aspects of 3D integration, including 3D circuit and system design, new processes and simulation techniques, alternative communication schemes for 3D circuits and systems, application of novel materials for 3D systems, and the thermal challenges to restrict power dissipation and improve performance of 3D systems. Containing contributions from experts in industry as well as academia, this authoritative text: Illustrates different 3D integration approaches, such as die-to-die, die-to-wafer, and wafer-to-wafer Discusses the use of interposer technology and the role of Through-Silicon Vias (TSVs) Presents the latest improvements in three major fields of thermal management for multiprocessor systems-on-chip (MPSoCs) Explores ThruChip Interface (TCI), NAND flash memory stacking, and emerging applications Describes large-scale integration testing and state-of-the-art low-power testing solutions Complete with experimental results of chip-level 3D integration schemes tested at IBM and case studies on advanced complementary metal-oxide-semiconductor (CMOS) integration for 3D integrated circuits (ICs), Design of 3D Integrated Circuits and Systems is a practical reference that not only covers a wealth of

design issues encountered in 3D integration but also demonstrates their impact on the efficiency of 3D systems.

**Digital Integrated Circuit Design** - Kenneth William Martin 2000

The impact of digital integrated circuits on our modern society has been pervasive. They are the enabling technology of the current computer and information-technology revolution. This is largely true because of the immense amount of signal and computer processing that can be realized in a single integrated circuit; modern IC's may contain millions of logic gates. This text book is intended to take a reader having only a minimal background and knowledge in electronics to the point where they can design state-of-the-art digital integrated circuits. Designing high-performance digital integrated circuits requires expertise in many different areas. These include semiconductor physics, integrated circuit processing, transistor-level design, logic-level design, system-level design, testing, etc. Aspects of these topics are covered throughout this text, although the emphasis is on transistor-level design of digital integrated circuits and systems. This is in contrast to the perspective in many other texts, which takes a system-level or VLSI approach where transistor-level details are minimized. It is the author's belief that before system-level considerations can be properly evaluated, an in-depth transistor-level understanding must first be obtained. Important system-level considerations such as timing, pipe-lining, clock distribution, and system building blocks are covered in detail, but the emphasis on transistors first. Throughout the book, physical and intuitive explanations are given, and although mathematical quantitative analysis of many circuits have necessarily been presented, Martin has attempted not to "miss seeing the forest because of the trees". This book presents the critical underlying concepts without becoming entangled in tedious and over-complicated circuit analyses. It is intended for senior/graduate level students in electrical and computer engineering. This course assumes the Sedra/Smith Microelectronic Circuits course as a prerequisite.

**Berkshire Encyclopedia of Sustainability 7/10** - Ray C. Anderson 2012-11-01

China, India, and East and Southeast Asia: Assessing Sustainability provides unprecedented analyses by regional experts and scholars elsewhere in the world on China, India, and their neighbors. Despite growing demands internally on their natural resources (China and India alone are home to more than one-third of the world's population), the expanding global economic influence of this region makes these countries vital players in a sustainable future for all citizens of the Earth. Regional coverage includes topics such as business and commerce, environmental and corporate law, and lifestyles and values.

**Introduction to VLSI Circuits and Systems** - John P. Uyemura 2002

CD-ROM contains: AIM SPICE (from AIM Software) -- Micro-Cap 6 (from Spectrum Software) -- Silos III Verilog Simulator (from Simucad) -- Adobe Acrobat Reader 4.0 (from Adobe).

**Design Through Verilog HDL** - T. R. Padmanabhan 2003-11-05

A comprehensive resource on Verilog HDL for beginners and experts Large and complicated digital circuits can be incorporated into hardware by using Verilog, a hardware description language (HDL). A designer aspiring to master this versatile language must first become familiar with its constructs, practice their use in real applications, and apply them in combinations in order to be successful. Design Through Verilog HDL affords novices the opportunity to perform all of these tasks, while also offering seasoned professionals a comprehensive resource on this dynamic tool. Describing a design using Verilog is only half the story: writing test-benches, testing a design for all its desired functions, and how identifying and removing the faults remain significant challenges. Design Through Verilog HDL addresses each of these issues concisely and effectively. The authors discuss constructs through illustrative examples that are tested with popular simulation packages, ensuring the subject matter remains practically relevant. Other important topics covered include: Primitives Gate and Net delays Buffers CMOS switches State machine design Further, the authors focus on illuminating the differences between gate level, data flow, and behavioral styles of Verilog, a critical distinction for designers. The book's final chapters deal with advanced topics such as timescales, parameters and related constructs, queues, and switch level design. Each chapter concludes with exercises that both ensure readers have mastered the present material and stimulate readers to explore avenues of their own choosing. Written and assembled in a paced, logical manner, Design Through Verilog HDL provides professionals, graduate students, and advanced undergraduates with a one-of-a-kind resource.

**Secure Smart Embedded Devices, Platforms and Applications** - Konstantinos Markantonakis

2013-09-14

New generations of IT users are increasingly abstracted from the underlying devices and platforms that provide and safeguard their services. As a result they may have little awareness that they are critically dependent on the embedded security devices that are becoming pervasive in daily modern life. *Secure Smart Embedded Devices, Platforms and Applications* provides a broad overview of the many security and practical issues of embedded devices, tokens, and their operation systems, platforms and main applications. It also addresses a diverse range of industry/government initiatives and considerations, while focusing strongly on technical and practical security issues. The benefits and pitfalls of developing and deploying applications that rely on embedded systems and their security functionality are presented. A sufficient level of technical detail to support embedded systems is provided throughout the text, although the book is quite readable for those seeking awareness through an initial overview of the topics. This edited volume benefits from the contributions of industry and academic experts and helps provide a cross-discipline overview of the security and practical issues for embedded systems, tokens, and platforms. It is an ideal complement to the earlier work, *Smart Cards Tokens, Security and Applications* from the same editors.

**CMOS Digital Integrated Circuits** - Sung-Mo Kang 2002

The fourth edition of *CMOS Digital Integrated Circuits: Analysis and Design* continues the well-established tradition of the earlier editions by offering the most comprehensive coverage of digital CMOS circuit design, as well as addressing state-of-the-art technology issues highlighted by the widespread use of nanometer-scale CMOS technologies. In this latest edition, virtually all chapters have been re-written, the transistor model equations and device parameters have been revised to reflect the significant changes that must be taken into account for new technology generations, and the material has been reinforced with up-to-date examples. The broad-ranging coverage of this textbook starts with the fundamentals of CMOS process technology, and continues with MOS transistor models, basic CMOS gates, interconnect effects, dynamic circuits, memory circuits, arithmetic building blocks, clock and I/O circuits, low power design techniques, design for manufacturability and design for testability.

*Introduction to Digital Electronics* - J. Crowe 1998-03-27

This text takes the student from the very basics of digital electronics to an introduction of state-of-the-art techniques used in the field. It is ideal for any engineering or science student who wishes to study the subject from its basic principles as well as serving as a guide to more advanced topics for readers already familiar with the subject. The coverage is sufficiently in-depth to allow the reader to progress smoothly onto higher level texts.

**Design with Operational Amplifiers and Analog Integrated Circuits** - Sergio Franco 2003-07-01

Franco's "Design with Operational Amplifiers and Analog Integrated Circuits, 4e" combines theory with real-life applications to deliver a straightforward look at analog design principles and techniques. An emphasis on the physical picture helps the student develop the intuition and practical insight that are the keys to making sound design decisions. The book is intended for a design-oriented course in applications with operational amplifiers and analog ICs. It also serves as a comprehensive reference for practicing engineers. This new edition includes enhanced pedagogy (additional problems, more in-depth coverage of negative feedback, more effective layout), updated technology (current-feedback and folded-cascode amplifiers, and low-voltage amplifiers), and increased topical coverage (current-feedback amplifiers, switching regulators and phase-locked loops).

*Anal og Ci rcui t Desi gn* Sergio Franco 2014-05-01

Places emphasis on developing intuition and physical insight. This title includes numerous examples and problems that have been carefully thought out to promote problem solving methodologies of the type engineers apply daily on the job.

**FUNDAMENTALS OF DIGITAL CIRCUITS** - A. ANAND KUMAR, 2016-07-18

The Fourth edition of this well-received text continues to provide coherent and comprehensive coverage of digital circuits. It is designed for the undergraduate students pursuing courses in areas of engineering disciplines such as Electrical and Electronics, Electronics and Communication, Electronics and Instrumentation, Telecommunications, Medical Electronics, Computer Science and Engineering, Electronics, and Computers and Information Technology. It is also useful as a text for MCA, M.Sc.

(Electronics) and M.Sc. (Computer Science) students. Appropriate for self study, the book is useful even for AMIE and grad IETE students. Written in a student-friendly style, the book provides an excellent introduction to digital concepts and basic design techniques of digital circuits. It discusses Boolean algebra concepts and their application to digital circuitry, and elaborates on both combinational and sequential circuits. It provides numerous fully worked-out, laboratory tested examples to give students a solid grounding in the related design concepts. It includes a number of short questions with answers, review questions, fill in the blanks with answers, multiple choice questions with answers and exercise problems at the end of each chapter.

*Fundamentals of Modern VLSI Devices* - Yuan Taur 2013-05-02

Learn the basic properties and designs of modern VLSI devices, as well as the factors affecting performance, with this thoroughly updated second edition. The first edition has been widely adopted as a standard textbook in microelectronics in many major US universities and worldwide. The internationally renowned authors highlight the intricate interdependencies and subtle trade-offs between various practically important device parameters, and provide an in-depth discussion of device scaling and scaling limits of CMOS and bipolar devices. Equations and parameters provided are checked continuously against the reality of silicon data, making the book equally useful in practical transistor design and in the classroom. Every chapter has been updated to include the latest developments, such as MOSFET scale length theory, high-field transport model and SiGe-base bipolar devices.

*Digital Integrated Circuits* - Thomas A. DeMassa 1996

Contains the most extensive coverage of digital integrated circuits available in a single source. Provides complete qualitative descriptions of circuit operation followed by in-depth analytical analyses and spice simulations. The circuit families described in detail are transistor-transistor logic (TTL, STTL, and ASTTL), emitter-coupled logic (ECL), NMOS logic, CMOS logic, dynamic CMOS, BiCMOS structures and various GASFET technologies. In addition to detailed presentation of the basic inverter circuits for each digital logic family, complete details of other logic circuits for these families are presented.

*Digital Communications* - Bernard Sklar 2016-12-23

The clear, easy-to-understand introduction to digital communications Completely updated coverage of today's most critical technologies Step-by-step implementation coverage Trellis-coded modulation, fading channels, Reed-Solomon codes, encryption, and more Exclusive coverage of maximizing performance with advanced "turbo codes" "This is a remarkably comprehensive treatment of the field, covering in considerable detail modulation, coding (both source and channel), encryption, multiple access and spread spectrum. It can serve both as an excellent introduction for the graduate student with some background in probability theory or as a valuable reference for the practicing communication system engineer. For both communities, the treatment is clear and well presented." - Andrew Viterbi, The Viterbi Group Master every key digital communications technology, concept, and technique. *Digital Communications, Second Edition* is a thoroughly revised and updated edition of the field's classic, best-selling introduction. With remarkable clarity, Dr. Bernard Sklar introduces every digital communication technology at the heart of today's wireless and Internet revolutions, providing a unified structure and context for understanding them -- all without sacrificing mathematical precision. Sklar begins by introducing the fundamentals of signals, spectra, formatting, and baseband transmission. Next, he presents practical coverage of virtually every contemporary modulation, coding, and signal processing technique, with numeric examples and step-by-step implementation guidance. Coverage includes: Signals and processing steps: from information source through transmitter, channel, receiver, and information sink Key tradeoffs: signal-to-noise ratios, probability of error, and bandwidth expenditure Trellis-coded modulation and Reed-Solomon codes: what's behind the math Synchronization and spread spectrum solutions Fading channels: causes, effects, and techniques for withstanding fading The first complete how-to guide to turbo codes: squeezing maximum performance out of digital connections Implementing encryption with PGP, the de facto industry standard Whether you're building wireless systems, xDSL, fiber or coax-based services, satellite networks, or Internet infrastructure, Sklar presents the theory and the practical implementation details you need. With nearly 500 illustrations and 300 problems and exercises, there's never been a faster way to master advanced digital communications. CD-ROM INCLUDED The CD-ROM contains a complete educational

version of Elanix' SystemView DSP design software, as well as detailed notes for getting started, a comprehensive DSP tutorial, and over 50 additional communications exercises.

**Design and Analysis of High Efficiency Line Drivers for xDSL** - Tim Piessens 2006-04-18

Design and Analysis of High Efficiency Line Drivers for xDSL covers the most important building block of an xDSL (ADSL, VDSL, ...) system: the line driver. Traditional Class AB line drivers consume more than 70% of the total power budget of state-of-the-art ADSL modems. This book describes the main difficulties in designing line drivers for xDSL. The most important specifications are elaborated starting from the main properties of the channel and the signal properties. The traditional (class AB), state-of-the-art (class G) and future technologies (class K) are discussed. The main part of Design and Analysis of High Efficiency Line Drivers for xDSL describes the design of a novel architecture: the Self-Oscillating Power Amplifier or SOPA.

**Digital Integrated Circuit Design** - Hubert Kaeslin 2008-04-28

This practical, tool-independent guide to designing digital circuits takes a unique, top-down approach, reflecting the nature of the design process in industry. Starting with architecture design, the book comprehensively explains the why and how of digital circuit design, using the physics designers need to know, and no more.

**Piezoelectric Energy Harvesting** - Alper Erturk 2011-04-04

The transformation of vibrations into electric energy through the use of piezoelectric devices is an exciting and rapidly developing area of research with a widening range of applications constantly materialising. With Piezoelectric Energy Harvesting, world-leading researchers provide a timely and comprehensive coverage of the electromechanical modelling and applications of piezoelectric energy harvesters. They present principal modelling approaches, synthesizing fundamental material related to mechanical, aerospace, civil, electrical and materials engineering disciplines for vibration-based energy harvesting using piezoelectric transduction. Piezoelectric Energy Harvesting provides the first comprehensive treatment of distributed-parameter electromechanical modelling for piezoelectric energy harvesting with extensive case studies including experimental validations, and is the first book to address modelling of various forms of excitation in piezoelectric energy harvesting, ranging from airflow excitation to moving loads, thus ensuring its relevance to engineers in fields as disparate as aerospace engineering and civil engineering. Coverage includes: Analytical and approximate analytical distributed-parameter electromechanical models with illustrative theoretical case studies as well as extensive experimental validations Several problems of piezoelectric energy harvesting ranging from simple harmonic excitation to random vibrations Details of introducing and modelling piezoelectric coupling for various problems Modelling and exploiting nonlinear dynamics for performance enhancement, supported with experimental verifications Applications ranging from moving load excitation of slender bridges to airflow excitation of aeroelastic sections A review of standard nonlinear energy harvesting circuits with modelling aspects.

**Analysis and Design of Analog Integrated Circuits, 5th Edition** - Paul R. Gray 2009-01-05

This is the only comprehensive book in the market for engineers that covers the design of CMOS and bipolar analog integrated circuits. The fifth edition retains its completeness and updates the coverage of bipolar and CMOS circuits. A thorough analysis of a new low-voltage bipolar operational amplifier has been added to Chapters 6, 7, 9, and 11. Chapter 12 has been updated to include a fully differential folded cascode operational amplifier example. With its streamlined and up-to-date coverage, more engineers will turn to this resource to explore key concepts in the field.

**Field and Wave Electromagnetics** - Cheng 1989-09

**CMOS Logic Circuit Design** John P. Uyemura 2007-05-08

This is an up-to-date treatment of the analysis and design of CMOS integrated digital logic circuits. The self-contained book covers all of the important digital circuit design styles found in modern CMOS chips, emphasizing solving design problems using the various logic styles available in CMOS.

**Low Power Design Essentials** - Jan Rabaey 2009-04-21

This book contains all the topics of importance to the low power designer. It first lays the foundation and then goes on to detail the design process. The book also discusses such special topics as power management and modal design, ultra low power, and low power design methodology and flows. In addition, coverage includes projections of the future and case studies.

**Advanced Circuits for Emerging Technologies** - Krzysztof Iniewski 2012-04-17

The book will address the-state-of-the-art in integrated circuit design in the context of emerging systems. New exciting opportunities in body area networks, wireless communications, data networking, and optical imaging are discussed. Emerging materials that can take system performance beyond standard CMOS, like Silicon on Insulator (SOI), Silicon Germanium (SiGe), and Indium Phosphide (InP) are explored. Three-dimensional (3-D) CMOS integration and co-integration with sensor technology are described as well. The book is a must for anyone serious about circuit design for future technologies. The book is written by top notch international experts in industry and academia. The intended audience is practicing engineers with integrated circuit background. The book will be also used as a recommended reading and supplementary material in graduate course curriculum. Intended audience is professionals working in the integrated circuit design field. Their job titles might be : design engineer, product manager, marketing manager, design team leader, etc. The book will be also used by graduate students. Many of the chapter authors are University Professors.

**Solid State Pulse Circuits** - David A. Bell 2006-08-24

This volume extensively covers semiconductor pulse circuits, explaining circuit operation and analysis and discusses in detail practical pulse circuit design methods.

**CMOS VLSI Design : A circuits and systems perspective** - Neil H.E. Weste 2015

The fourth edition of the best-selling text details the modern techniques for the design of complex and high-performance CMOS systems on a chip. Covering the fundamentals of CMOS design from the digital systems level to the circuit level, this book explains the fundamental principles and is a guide to good design practices

**Design of Analog CMOS Integrated Circuits** - Behzad Razavi 2001

This textbook deals with the analysis and design of analog CMOS integrated circuits, emphasizing recent technological developments and design paradigms that students and practicing engineers need to master to succeed in today's industry. Based on the author's teaching and research experience in the past ten years, the text follows three general principles: (1) Motivate the reader by describing the significance and application of each idea with real-world problems; (2) Force the reader to look at concepts from an intuitive point of view, preparing him/her for more complex problems; (3) Complement the intuition by rigorous analysis, confirming the results obtained by the intuitive, yet rough approach.