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this textbook is intended for a senior level course in digital systems design the book covers both basic principles of digital system design and the use of a hardware description language verilog in the design process after basic principles are covered a variety of examples are used to illustrate the principles

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dec 23 2015 this article will go through the design process of creating a digital system by first defining a design problem second creating the computational model of the system as a finite state machine and third translating the fsm into the hardware description language vhdl vhdl is actually a double acronym

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